

Abstracts

A High-Performance and Miniaturized Dual-Use (Antenna/Local) GaAs SPDT Switch IC Operating at +3V/0V

H. Uda, K. Nogawa, T. Hirai, T. Sawai, T. Higashino and Y. Harada. "A High-Performance and Miniaturized Dual-Use (Antenna/Local) GaAs SPDT Switch IC Operating at +3V/0V." 1996 MTT-S International Microwave Symposium Digest 96.1 (1996 Vol. I [MWSYM]): 141-144.

We have developed an ultra-compact dual-use (antenna/local) switch IC for PHS operating at +3/0 V. This IC has a circuit configuration which utilizes MESFETS with two kinds of pinch-off voltages. Additional applied techniques include a circuit design method that employed electromagnetic field analysis, a pull-up method which utilizes forward current flowing in order through the gates of MESFETS, and high isolation characteristic obtained by use of a chip inductor. The insertion loss and isolation characteristics of this IC are, respectively, 0.54 dB and 28.4 dB at 1.9 GHz and 0.48 dB and 30.0 dB at 1.65 GHz. Furthermore, we were able to suppress adjacent channel leakage power to 61.5 dBc at 600-kHz offset during input power of 22 dBm QPSK modulated signals.

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